TEMPERATURE-CONTROLLED VOLTAGE REFERENCE – HVFXX

- REDUCTION OF OUTPUT VOLTAGE DRIFT DOWN TO MORE THAN 1/10 OF ORIGINAL SPEC

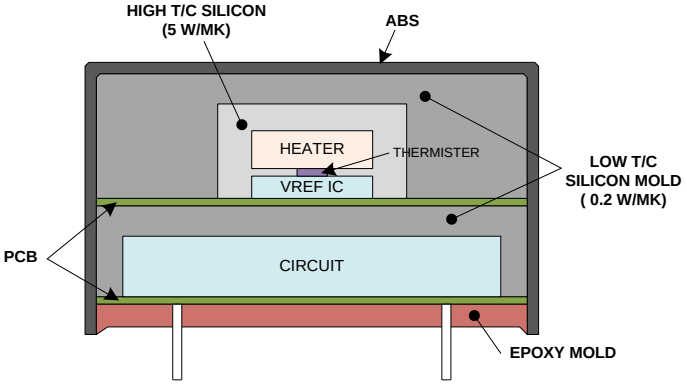
- PERFORMANCE WITH VREF IC:AD587JR(20PPM/°C$_{MAX}$) VAA:15V VBB:5V VCC:24V

OPERATING TEMPERATURE	5°C - 50°C
TEMPERATURE SET	63±3°C
TEMPRATURE CONTROL WIDTH	±0.25°C
OUTPUT VOLTAGE	10V
OUTPUT VOLTAGE TC	0.5 PPM/°C MAX
VREF IC DISSAPATION	< 100mW
WARMUP TIME	300S
OVERHEAT PROTECTION	110°C
CURRENT DRAWN FROM VCC	0 – 0.22A
CURRENT DRAWN FROM VBB	< 2.2mA (1.7mA$_{TYP}$)

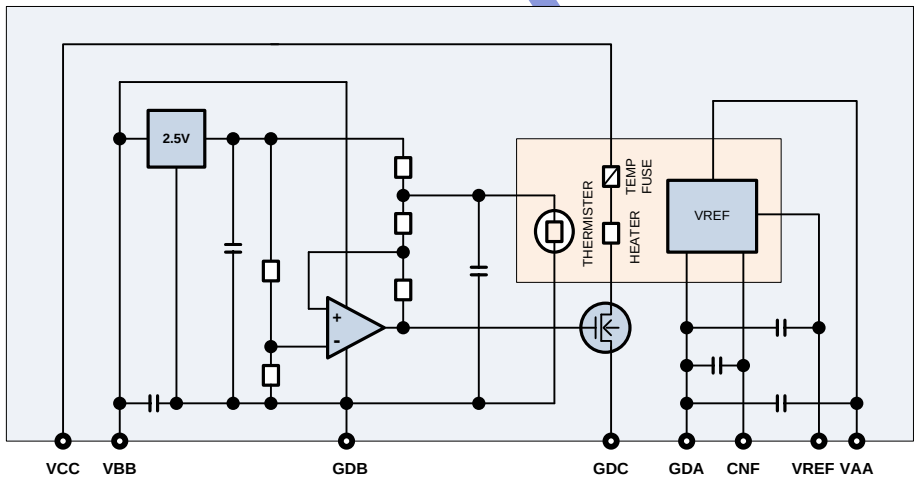
- REQUIREMENTS

VBB	5V±5%
TARGET TEMP.	65±2°C STANDARD (OPTIONAL >= 55°C <=75°C)
VREF IC	HVF01:AD587JR VREF:10V VAA:15V±10% VCC:24V±10%
	HVF02:LM4140CCM-4.1 VREF:4.096V VAA:5V±5% VCC:5V±5%
	OPTIONAL VREF IC NEEDS SO8 PACKAGED

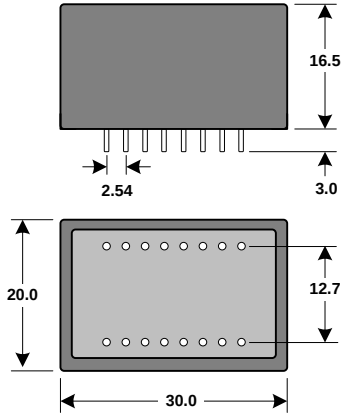
INTERNAL STRUCTURE



INTERNAL SCHEMATIC



OUTLINE



ELECTRO WORK